

REMARKS

In accordance with the foregoing, various of the claims have been amended to improve form and without the introduction of new matter. Approval and entry of the amended claims are respectfully requested.

Claims 1-15 are pending and under consideration.

ITEM 2: REJECTION OF CLAIMS 1, 5, 6 AND 10 FOR ANTICIPATION UNDER 35 U.S.C. §102(e) BY NAGATOME, U.S. PATENT 6,339,753; AND

ITEM 4: REJECTION OF CLAIM 11 UNDER 35 U.S.C. §103(a) FOR OBVIOUSNESS OVER NAGATOME

The rejections are respectfully traversed.

THE PRESENT INVENTION

Is believed to be beneficial to briefly discuss important features of the present invention, from which is believed it will be readily seen that Nagatome neither anticipates nor renders obvious the invention as defined in the pending claims.

In accordance with the present invention, drive power is supplied to a debugging circuit independently of the debug target circuit. Accordingly, in the present invention, supply of driving power to the debug target circuit can be stopped while the debugging circuit is in operation by independently supplying driving power to the debugging circuit. Therefore, debugging of the debug target circuit is completely performed at the time when the power to that circuit is turned off.

In accordance with the first embodiment of the invention shown in Figs. 2 and 3, first and second power supply terminals 32 and 33 provide power from an external source, separately and independently, to the debugged target circuit 4 and the debugging circuit 5, respectively, whereby the supply power to the first power supply terminal 32 maybe stopped while power continues to be supplied to the second power supply terminal 33 and thereby to the debugging circuit 5.

In a second embodiment shown in Fig. 5, a single external power supply terminal 61 is connected directly to the debugging circuit 5 and through a switch 62 to the debug target circuit 4, an external control signal being supplied to terminal 63 to selectively close the switch and supply power therethrough to the debug target circuit 4 or to open the switch and end the supply

of power. Other embodiments disclose alternative techniques for controlling the supply of power to the debug target circuit, independently of the supply of power to the debugging circuit 5.

NAGATOME U.S. PATENT 6,339,753 DISCLOSES NO MORE THAN THE ADMITTED PRIOR ART OF FIG. 1

Nagatome merely discloses, in Fig. 2, that designation unit 42, which corresponds to the debugging circuit in the present invention, and simulator chip 41, which corresponds to the debug target circuit in the present invention and simulates operation of microcomputer 21 controlled by the designation unit 42, both disposed within ICE 40, are driven by the same power source, namely power supply 43a. Nagatome thus discloses what applicants regard as the conventional art for the present invention, as shown in Fig. 1 of the present application.

Therefore, Nagatome, being no different from the admitted prior of Fig. 1 of the present application, cannot stop the supply of driving power to the simulator chip 41 when the designation unit 42 is in operation, being driven by the driving power supplied thereto.

Therefore, the present invention is neither anticipated by nor obvious in view of the cited reference.

CONCLUSION

In accordance with the foregoing, it is respectfully submitted that the pending claims distinguish patentably over the Nagatome reference.

There being no further outstanding objections or rejections, it is submitted that the application is in condition for allowance. An early action to that effect is courteously solicited.

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Finally, if there are any formal matters remaining after this response, the Examiner is requested to telephone the undersigned to attend to these matters.

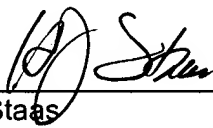
If there are any additional fees associated with filing of this Amendment, please charge the same to our Deposit Account No. 19-3935.

Respectfully submitted,

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By: _____


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